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Transactions On High Performance Embedded Architectures

High-Performance Transactions for Persistent Memories Aasheesh Kolli University of Michigan akolli@umich.edu Steven Pelley Snowflake Computing steven.pelley@snowflake.net Ali Saidi ARM ali.saidi@arm.com Peter M. Chen University of Michigan pmchen@umich.edu Thomas F. Wenisch University of Michigan twenisch@umich.edu Abstract

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This paper describes how we achieved two orders of magnitude speedup in TC performance and shows that a full Deuteronomy stack can achieve very high performance overall. Importantly, the resulting full stack is a system that caches data residing on secondary storage while exhibiting performance on par with main memory systems.

High Performance Transactions in Deuteronomy - Microsoft

IEEE TRANSACTIONS ON PARALLEL AND DISTRIBUTED SYSTEMS 1 Fault-Aware Runtime Strategies for High Performance Computing Yawei Li, Student Member, IEEE, Zhiling Lan*, Member, IEEE, Prashasta Gujrati, Member, IEEE, and

IEEE TRANSACTIONS ON PARALLEL AND DISTRIBUTED SYSTEMS 1

A synchronization technique and scheduling strategy is described, which allows us to run a batch process simultaneously with on-line transactions. The batch process and the transactions are serialized in such a way that consistency level 3 is achieved.

Report on the international workshop on high-performance

High Performance Transactions via Early Write Visibility Jose M. Faleiro Yale University jose.faleiro@yale.edu Daniel J. Abadi Yale University dna@cs.yale.edu Joseph M. Hellerstein UC Berkeley hellerstein@berkeley.edu ABSTRACT In order to guarantee recoverable transaction execution, database systems permit a transaction's writes to be ...

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