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AXI Reference Guide www.xilinx.com 5 UG761 (v13.1) March 7, 2011 Chapter 1 Introducing AXI for Xilinx System Development Introduction Xilinx has adopted the Advanced eXtensible Interface (AXI) protocol for Intellectual Property (IP) cores beginning with the Spartan-6 and Virtex-6 devices. This document is intended to:

AXI Reference Guide - Xilinx - All Programmable

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About the Author Howard Fosdick Howard Fosdick has performed DBA and systems support work as an

independent consultant for 15 years. He's coded in Rexx for nearly two decades and has worked in most other major scripting lan-

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